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Bib Data Sheet

CONFIRMATION NO. 9948

SERIAL NUMBER 10/755,395	FILING DATE 01/13/2004 RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. 843.43390X00
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APPLICANTS

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** CONTINUING DATA ***** None S.W.S.

** FOREIGN APPLICATIONS ***** S.W.S.
JAPAN 2003-023151 01/31/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/14/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>S.W.S.</i> Initials	JAPAN	12	17	2

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TITLE

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE USING THERMAL TREATMENT THAT
FEATURES LOWER SPEED WAFER ROTATION AT LOW TEMPERATURES AND HIGHER SPEED WAFER
ROTATION AT HIGH TEMPERATURES

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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